#### EE 492 Biweekly Report 10

3/1/2021 - 3/15/2021

Group Number: SD May 21-43

Project Title: Emergency! Need backup!

Client/Advisor: Collins Aerospace / Andrew Bolstad

#### **Team Members / Role:**

James Curtis / Meeting Scribe

Caroline Easley / Meeting Facilitator

Marcelo Abrantes / Engineer (Power Systems)

Michael Kuehn / Communications Director

Benjamin Welte / Project Documentation

Abbey Wilder / Test Engineer

Stepan Zelenin / Engineer (Communication Systems)

#### **Period Summary:**

During this work period, our development PCB arrived which enabled us to begin testing our parts together. We soldered the parts on to the PCB using a solder oven provided by ETG, and we soldered the local oscillator onto its own board so that we could begin testing the code we have written for it. We began using the development board to test various parts for the final design; specifically, we tested the filters and were going to test the mixer but were prevented from doing so because we did not have inductors small enough to implement the test setup provided in the documentation.

With regard to the SNMP control circuit, testing began on the local oscillator, and the team was able to successfully set up a test circuit for the oscillator and verify its I2C address using a simple test script that probed all 128 possible address values. Several team members also

unsuccessfully attempted to implement the SNMP set command in SNMPv1. After talking to our client, we obtained permission to use SNMPv1 for our project's final deliverable instead of SNMPv3 because we were able to find an open-source library which implemented the get command in SNMPv1. Our plan is to improve on this library to eventually have the full functionality required for our project: the get and set SNMP commands.

### **Past Period Accomplishments:**

- Set up LO test circuit Ben, Stepan
- Found LO's I2C address w/test script Ben
- Soldered dev PCB using oven Marcelo, James, Caroline
- Additional RF simulation Stepan
- Revised test plan Marcelo, Stepan
- Filter testing Marcelo, James, Michael

#### **Pending Issues:**

- Program LO to output at each required frequency Ben
- Implement SNMP set command Abbey, Caroline
- Test mixer James, Marcelo, Caroline, Stepan
- Obtain high frequency test equipment Michael

#### **Individual Contributions:**

Name	Individual Contributions	Hours this week	Hours cumulative
James R.	• Soldered dev PCB using	12	42
	reflow oven		
	• Tested filters		
Caroline E.	Soldered dev PCB	12	43
	• Reviewed datasheets		

	• Researched RF testing		
	• Researched SNMP set		
	command		
Marcelo A.	Soldered dev PCB	12	49
	• RF PCB research		
	• Filter testing		
Michael K.	• Filter testing	12	42
	• RF PCB research		
	• Revised test plans		
Ben W.	Researched SNMP set	14	45
	command		
	• Obtained LO's I2C		
	address		
	• Devised LO test circuit		
Abbey W.	• Wrote & debugged 1 <sup>st</sup>	12	42
	iteration of SNMP set		
	command		
	• Researched solutions for		
	SNMP set command		
Stepan Z.	• Revised test plan	12	43
	• RF simulation		

## **Plans for the Upcoming Period:**

During the upcoming work period, we plan to finish writing the code for the local oscillator, finish testing our components on the dev PCB board (specifically the mixer), and hopefully implement the SNMP set command. We would then eventually like to begin integration testing once each of the radio's components are functioning individually.

## **Advisor Meeting Summary:**

While meeting with Dr. Bolstad, we discussed several of the roadblocks our team faced during the past report period including not being able to test our parts until the development PCB arrived and the lack of success implementing the SNMP set command. After discussing with Dr. Bolstad, the decision was made to ask our client's permission to use SNMPv1 instead of SNMPv3 for our project's final deliverable because version 1 has better open source libraries available.

## **Appendix (Images):**

Local Oscillator Test Circuit:



Local Oscillator Output Signal:



# Mixer Test Circuit:



RF Mixer Output (transmitter signal chain):



RF Mixer Output (receiver signal chain):

